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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/756,763	01/14/2004	Masahiro Sunohara	040001	1523

23850 7590 08/14/2006

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EXAMINER

WILLIAMS, ALEXANDER O

ART UNIT PAPER NUMBER

2826

DATE MAILED: 08/14/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/756,763

Applicant(s)

SUNOHARA ET AL.

Examiner

Alexander O. Williams

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 7/27/06.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) 1 to 12 and 14 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 13 and 15-17 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

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Serial Number: 10/756763 Attorney's Docket #: 040001

Filing Date: 1/14/2004; foreign priority to 1/23/03 and 11/17/03

Applicant: Sunohara et al.

Examiner: Alexander Williams

Applicant's RCE filed 7/27/06 has been acknowledged.

Applicant's Amendment filed 7/12/06 to the election of species I, figures 2A-2H (claims 13 and 15 to 17), filed 10/25/05 to the election of Group I, claims 13-17 filed 7/29/05 has been acknowledged.

This application contains claims 1 to 12 and 14 drawn to an invention non-elected with traverse. A complete response to the final rejection must include cancellation of non-elected claims or other appropriate action (see 37 CFR § 1.144 & MPEP § 821.01).

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent --

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Patentability shall not be negated by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).

Claims 13 and 15 to 17 are rejected under 35 U.S.C. § 102(e) as being anticipated by Higashi et al. (U.S. Patent Application Publication # 2003/0102547 A1). 13. Higashi et al. (figures 1 to 8) specifically figure 4 show an electronic parts packaging structure comprising: a wiring substrate **32b** including a wiring pattern **38**; a first insulation film **32a** formed on the wiring substrate; an electronic parts (**12**) having a connection terminal (**inherent**) on an element formation surface, the electronic parts being buried in the first insulation film in a state where the connection terminal is directed upward and being mounted in a state where a backside of the electronic parts is not in contact with the wiring substrate, and a lower portion of the first insulating film exists between the electronic parts and the wiring substrate; a second insulation film **32b** for covering the electronic parts, and whose upper surface is flat over a whole on the wiring substrate, and the second insulation film contacting with the electronic parts as a single layer, and; first via holes formed in a predetermined portion of the first and second insulation films on the wiring pattern, and having an identical inner surface; second via holes formed in a portion of the second insulating film on the connection terminal of the electronic parts; via holes **32** respectively formed in a predetermined portion of the first and second insulation films on the wiring pattern and the connection terminal; an upper wiring pattern **22** formed as a single wiring on the second insulation film, the upper wiring pattern being connected to the wiring pattern and the connection terminal through the via holes; and an upper electronic part (**top 12**) with bumps **38** which are flip-chip bonded to connection portions of the upper wiring pattern, the connection pad which directly contacts the upper surface of the second insulating film.

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15. The electronic parts packaging structure according to claims 13 or 14, Higashi et al. show wherein the element formation surface of the electronic parts and an upper surface of the insulation film in which the electronic parts is buried are at an almost same height to be planarized.

16. The electronic parts packaging structure according to claims 13 or 14, Higashi et al. show wherein the electronic parts is a semiconductor chip having a thickness of approximately 150 micrometer or less.

Note that the specification contains no disclosure of either the critical nature of the claimed dimensions or any unexpected results arising therefrom. Where patentability is said to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. In re Woodruff, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

17. The electronic parts packaging structure according to claims 13 or 14, Higashi et al. show wherein the insulation film is made of resin.

## Response

Applicant's arguments filed 7/12/06 have been fully considered, but are moot in view of the new grounds of rejections detailed above.

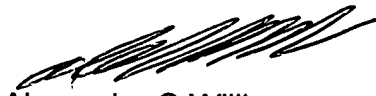
Field of Search	Date
U.S. Class and subclass: 257/774,773,758,211,203,207,208,730,700,701,698,e23.0 11,e23.178,e23.002,e23.181,e23.03,e25.031,e25.013 523/176	12/9/05 5/9/06 8/7/06
Other Documentation: foreign patents and literature in 257/774,773,758,211,203,207,208,730,700,701,698,e23.0 11,e23.178,e23.002,e23.181,e23.03,e25.031,e25.013 523/176	12/9/05 5/9/06 8/7/06
Electronic data base(s): U.S. Patents EAST	12/9/05 5/9/06 8/7/06

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alexander O. Williams whose telephone number is (571) 272 1924. The examiner can normally be reached on M-F 6:30AM-7:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272 1915. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Alexander O Williams  
Primary Examiner  
Art Unit 2826

AOW  
8/7/06